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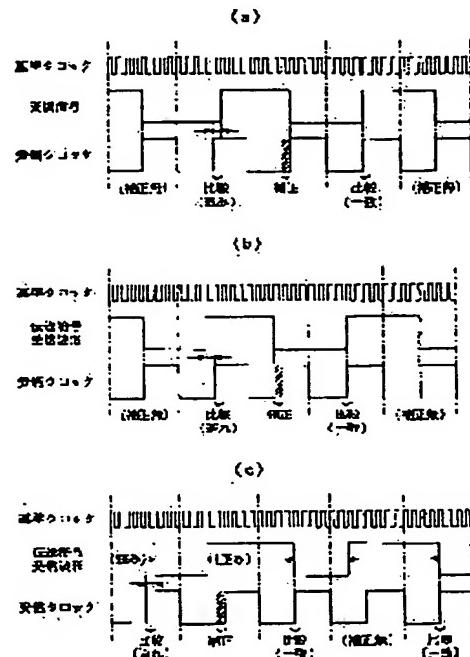
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(54) CLOCK SYNCHRONIZATION CORRECTION METHOD AND SYNCHRONIZING CLOCK GENERATOR

(57)Abstract:

PROBLEM TO BE SOLVED: To provide a clock synchronization method and a synchronizing clock generator that apply no meaningless and harmful phase correction to a generated clock even when the object signal to be synchronized has distortion.

SOLUTION: A received clock is generated by using a counter which is reset every time counting a count equivalent for a 2-code period, a phase of a received signal is compared with the received clock when the count equivalent to one-code period is counted after the reset, and increasing/decreasing the count to be reset according to the result of the phase comparison adjusts the phase of the received clock. Since the phase comparison is made for each 2-code period including both signal levels without fail, even when the received signal is subjected to waveform distortion of changing its duty ratio, the effect of the waveform distortion is cancelled, the effect of waveform distortion at the phase comparison is not caused so as to prevent useless phase correction against the waveform distortion.



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